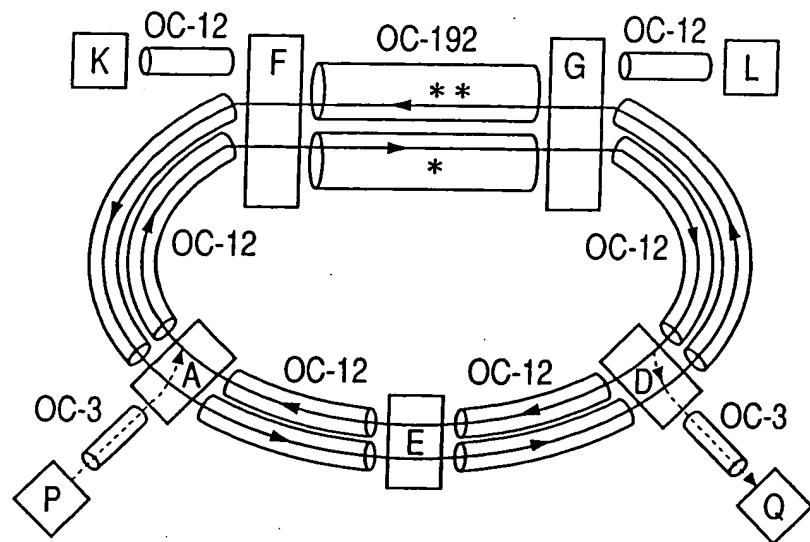
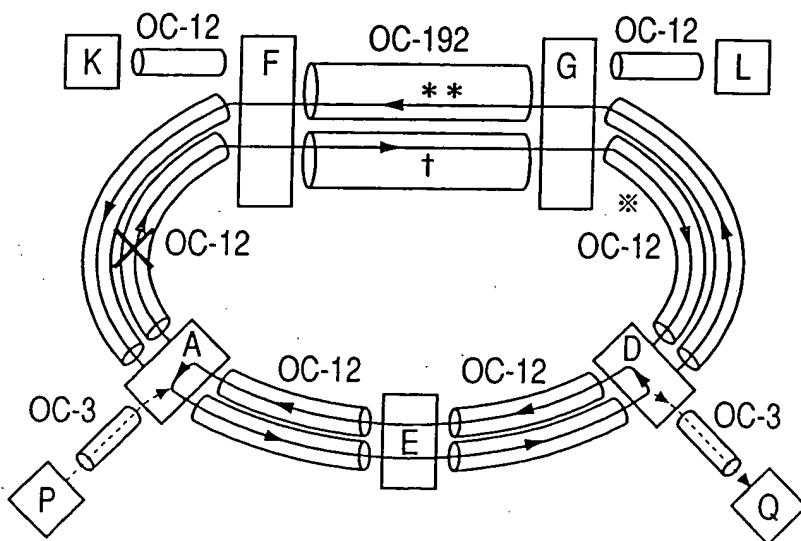


FIG. 1



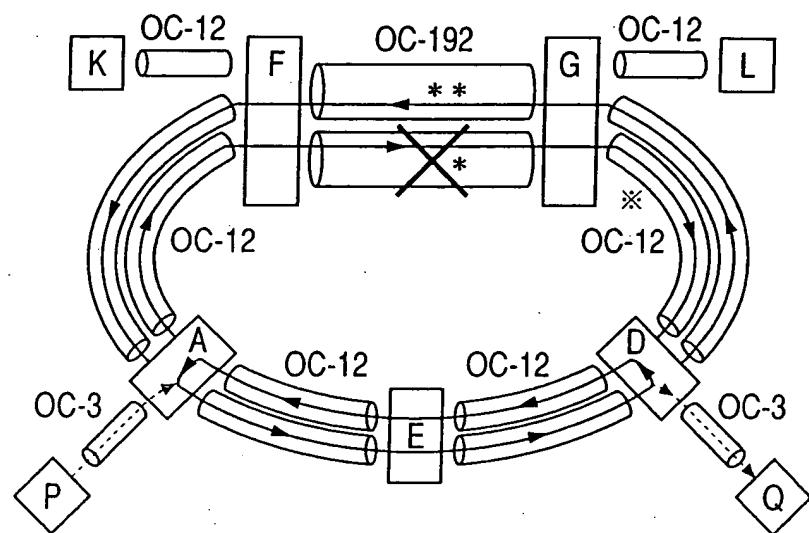
\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A  
 \*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

FIG. 2



\*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D  
 † : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex  
 \* : K1 BYTE=K2 BYTE=(FF)hex

FIG. 3



\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A  
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A

\*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

\*\*\* : K1 BYTE=K2 BYTE=(FF)hex

FIG. 4

OVERHEAD	FUNCTION
SECTION OVERHEAD	A1, 2A FRAME SYNCHRONIZATION
	B1 ADMINISTRATION OF ERRORS IN SECTION INTERVAL
	D1~D3 USED FOR MAINTENANCE OPERATION
	E1 SPEECH COMMUNICATION FOR CRAFT MAN
	J0 (C1) DESIGNATION OF OC-1 MULTIPLEX NUMBER OF OC-M SIGNAL
	F1 USED FOR CONVENIENCE OF CRAFT MAN
LINE OVERHEAD	H1, H2 INDICATION OF HEAD PHASE OF PATH
	H3 USED FOR FREQUENCY SYNCHRONIZATION
	B2, M1 ADMINISTRATION OF ERRORS IN LINE INTERVAL
	K1, K2 SWITCHING CONTROL FOR LINE INTERVAL, TRANSFER OF ALARM
	D4~D12 USED FOR MAINTENANCE OPERATION
	E2 SPEECH COMMUNICATION FOR CRAFT MAN
	S1 USED FOR OPERATION OF NETWORK SYNCHRONIZATION
	Z1, Z2 INTERNATIONALLY RESERVED AS SPARE

FIG. 5A

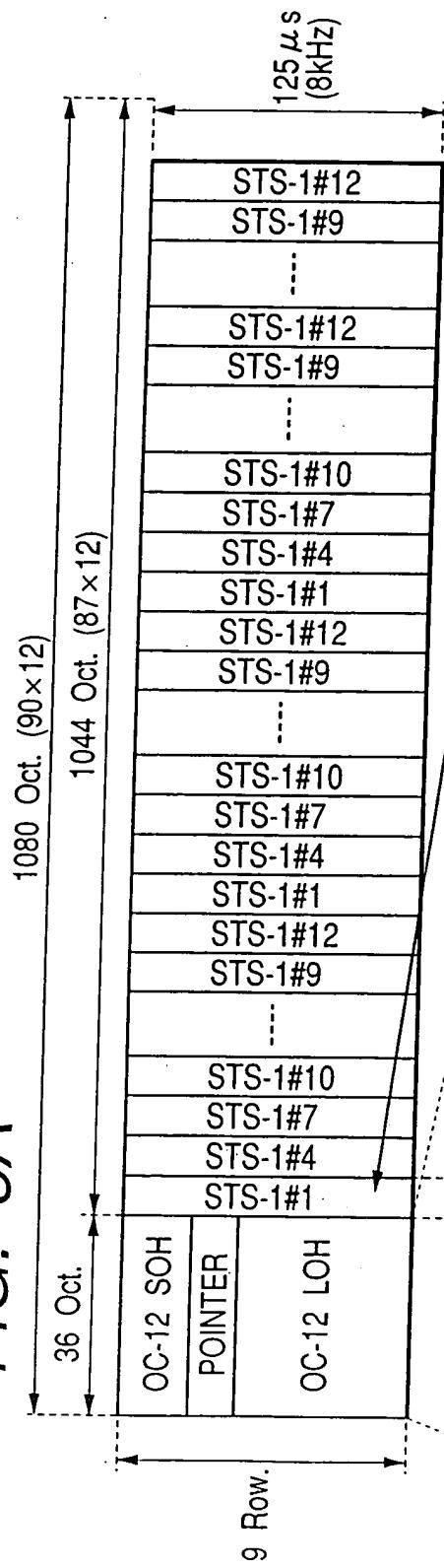


FIG. 5B

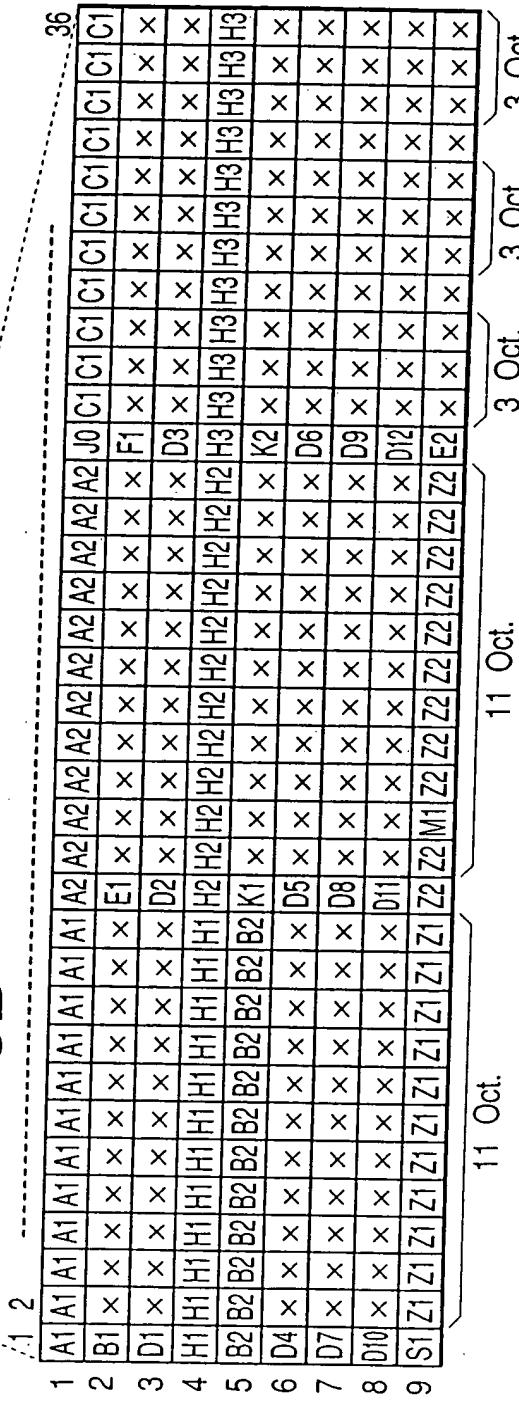


FIG. 5C

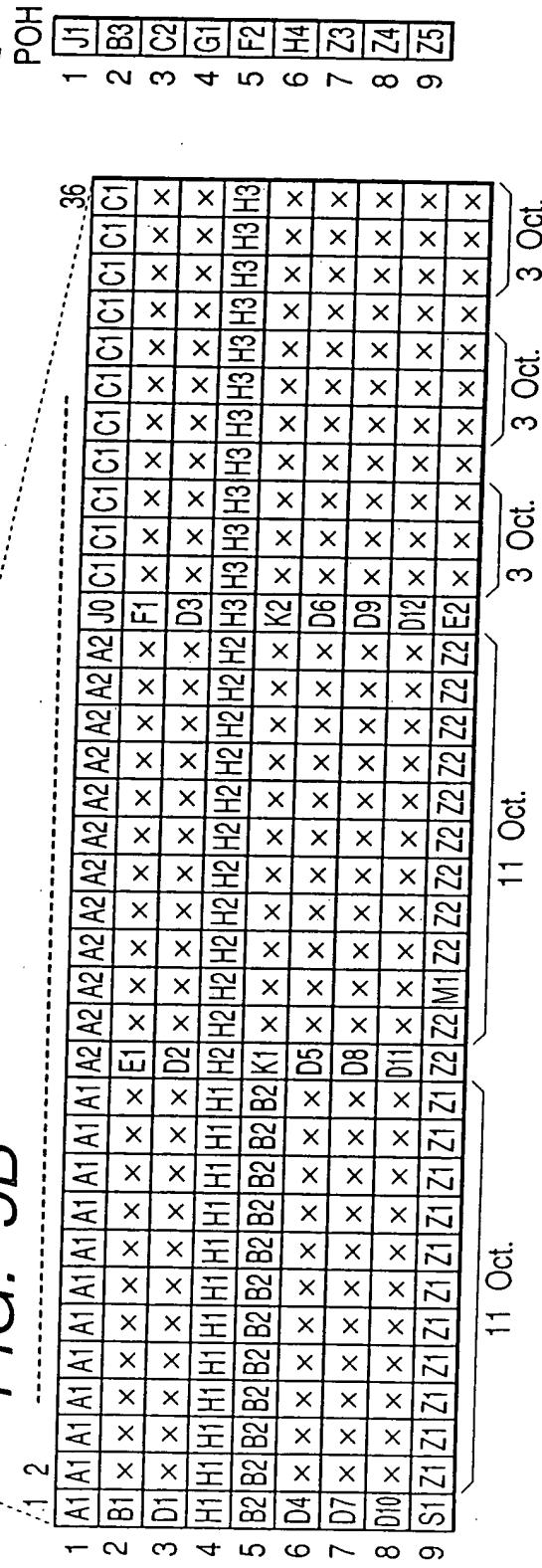


FIG. 6A

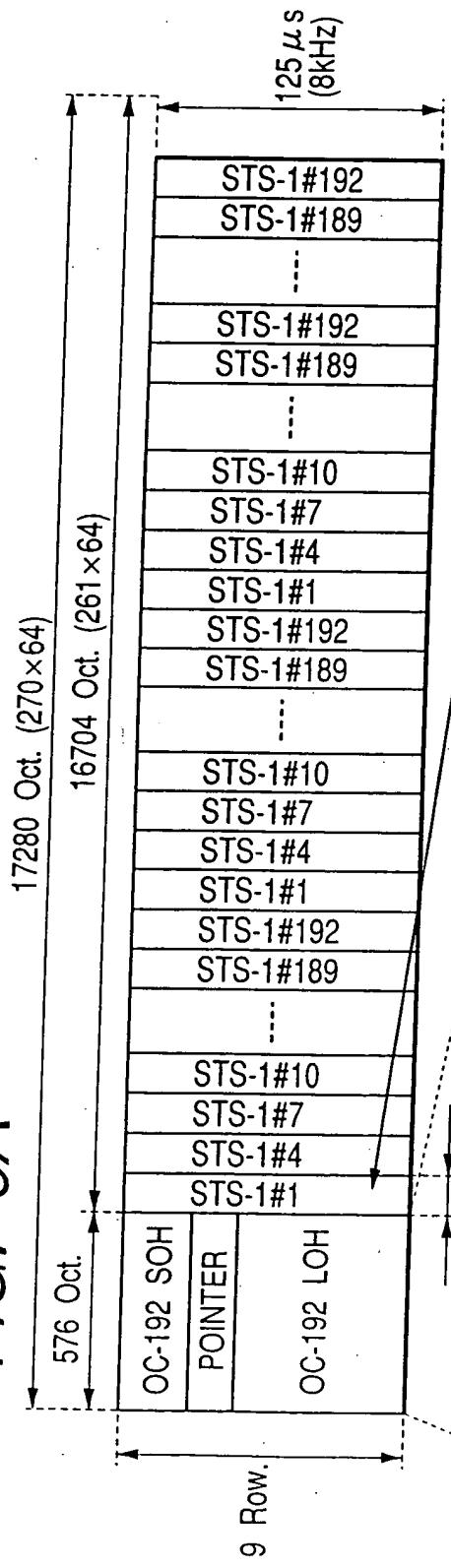


FIG. 6B

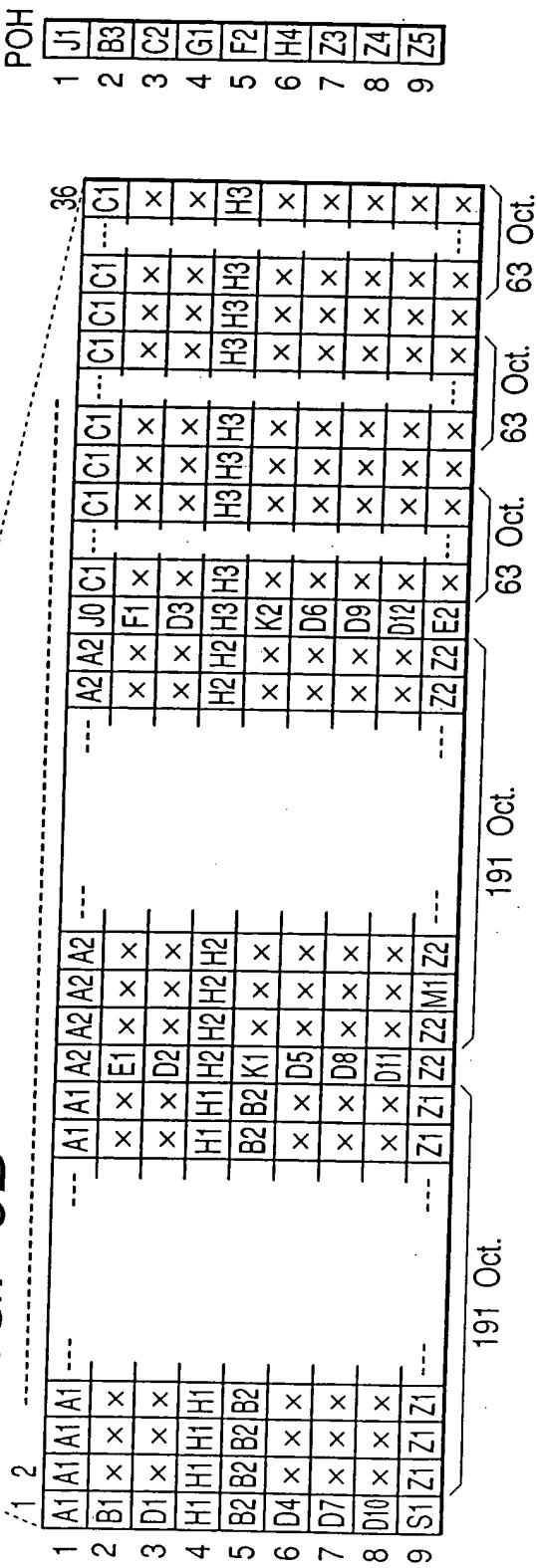


FIG. 6C

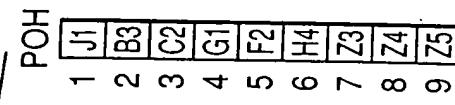


FIG. 7

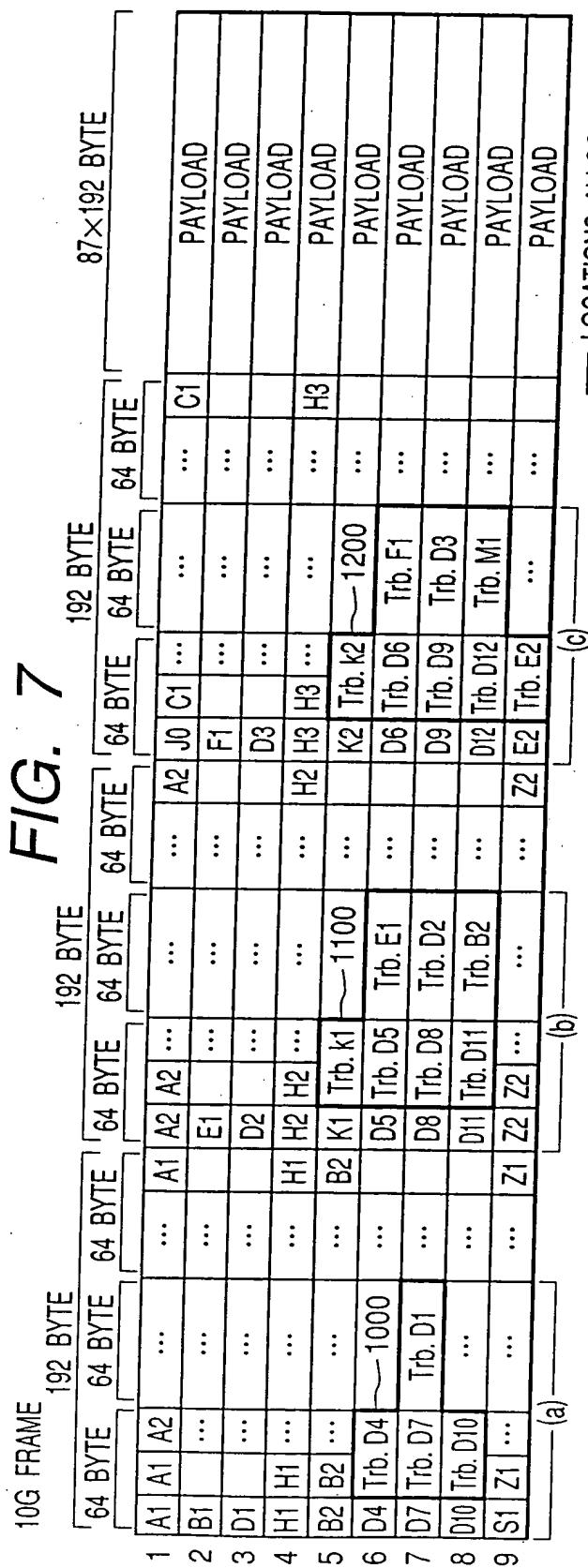
□ LOCATIONS ALLOCATED TO  
THROUGH BYTES

FIG. 8

(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	A1	A1	A1	A1	A1	...	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	...	A1	A1	A1	A1	A1	A1	...	...	...	...	
2	B1	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	
3	D1	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	
4	H1	H1	H1	H1	H1	...	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	H1	...	H1	H1	H1	H1	H1	H1	...	...	...	
5	B2	B2	B2	B2	B2	...	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	...	B2	B2	B2	B2	B2	B2	...	...	...	
6	D4	D4	D4	D4	D4	...	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	T#3	...	T#1	T#1	T#1	T#1	T#1	T#1	...	...	...	
7	D7	D7	D7	D7	D7	...	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	D7	...	D1	D1	D1	D1	D1	D1	...	...	...	
8	D10	D10	D10	D10	D10	...	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	T#2	...	T#1	T#1	T#1	T#1	T#1	T#1	...	...	...	
9	S1	Z1	Z1	Z1	Z1	...	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	Z1	...	Z1	Z1	Z1	Z1	Z1	Z1	...	...	...	

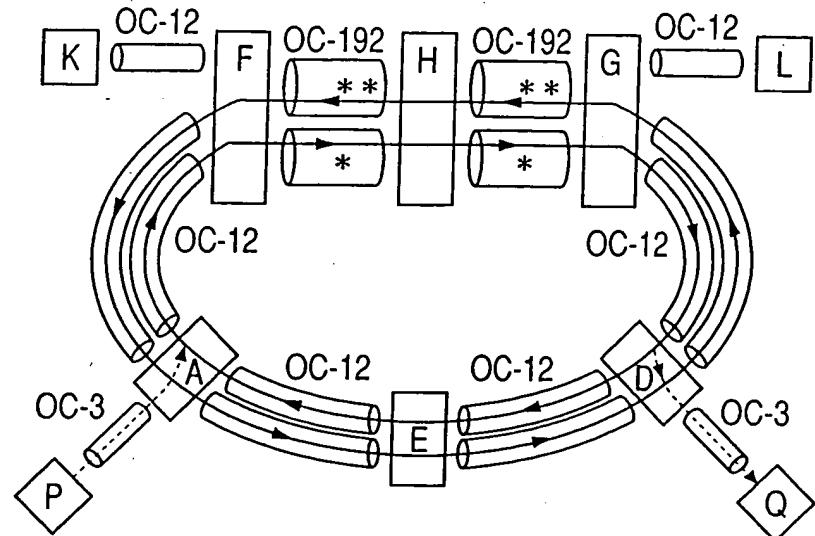
FIG. 9

	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	A2	A2	A2	A2	A2	A2	...	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	A2	...	A2	A2	A2	A2	A2	A2	...				
2	E1						...																...							...			
3	D2						...																...							...			
4	H2	H2	H2	H2	H2	H2	...	H2	H2	H2	H2	H2	H2	H2	H2	H2	H2	H2	H2	H2	H2	...	H2	H2	H2	H2	H2	H2	...				
5	K1						...	K1			K1											...							...				
6	D5						...	D5			D5											...							...				
7	D8						...	D8			D8											...							...				
8	D11						...	D11			D11										...							...					
9	Z2	Z2	M1	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	Z2	Z2	...				

FIG. 10

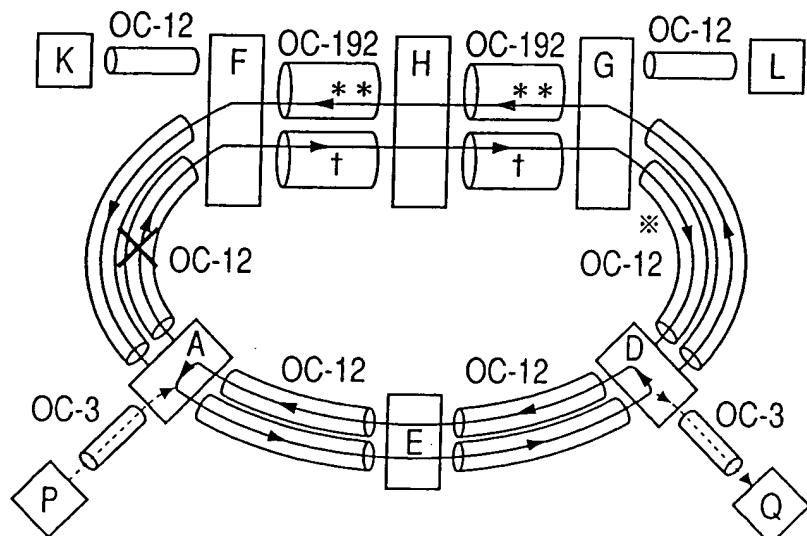
	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	...	(13)	...	(61)	(62)	(63)	(64)	(BYTE)
1	J0	C1	C1	C1	C1	C1	...	C1	C1	C1	C1	...	C1	C1	C1	C1	C1	C1	C1	C1	C1	...	C1	C1	C1	...	C1	C1	C1	...	C1		
2	F1						...					...										...			...				...			...	
3	D3						...					...										...			...				...			...	
4	H3	H3	H3	H3	H3	H3	...	H3	H3	H3	H3	...	H3	H3	H3	H3	H3	H3	H3	H3	H3	...	H3	H3	H3	...	H3	H3	H3	...	H3		
5	K2						...	K2			K2	...	T#1	T#2	T#3	T#4	T#5	T#6	T#7	T#8	T#9	T#10	...	T#11	T#12	T#13	...	F1			...	F1	
6	D6						...	D6			D6	...	T#1	T#2	T#3	T#4	T#5	T#6	T#7	T#8	T#9	T#10	...	T#11	T#12	T#13	...	D3			...	D3	
7	D9						...	D9			D9	...	T#1	T#2	T#3	T#4	T#5	T#6	T#7	T#8	T#9	T#10	...	T#11	T#12	T#13	...	D3			...	T#15	
8	D12						...	D12			D12	...	T#1	T#2	T#3	T#4	T#5	T#6	T#7	T#8	T#9	T#10	...	T#11	T#12	T#13	...	M1			...	M1	
9	E2						...	E2			E2	...	T#1	T#2	T#3	T#4	T#5	T#6	T#7	T#8	T#9	T#10	...	T#11	T#12	T#13	...	T#15			...	T#15	

FIG. 11



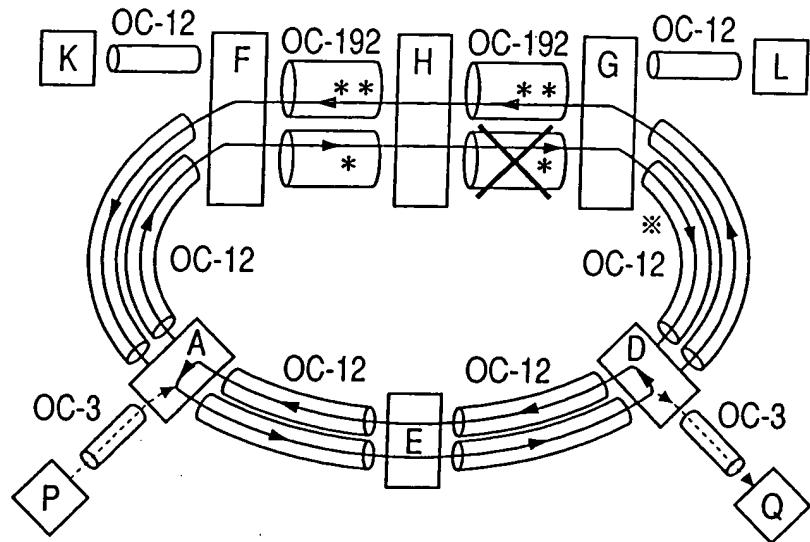
\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A  
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A  
\*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D

FIG. 12



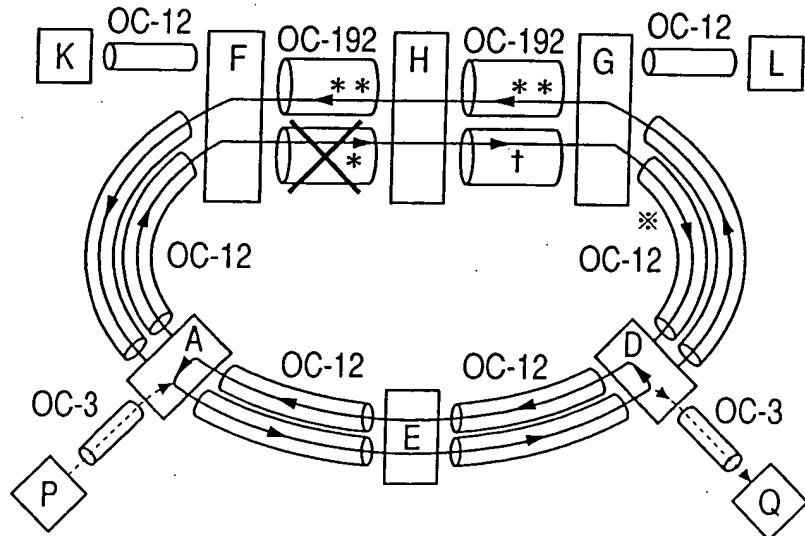
\* \* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D  
† : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex  
※ : K1 BYTE=K2 BYTE=(FF)hex

FIG. 13



\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A  
 \*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D  
 \* : K1 BYTE=K2 BYTE=(FF)hex

FIG. 14



\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS A  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS A  
 \*\* : K1 Tr#i BYTE=K1 BYTE FROM APPARATUS D  
 K2 Tr#i BYTE=K2 BYTE FROM APPARATUS D  
 † : K1 Tr#i BYTE=K2 Tr#i BYTE=(FF)hex  
 \* : K1 BYTE=K2 BYTE=(FF)hex

FIG. 15

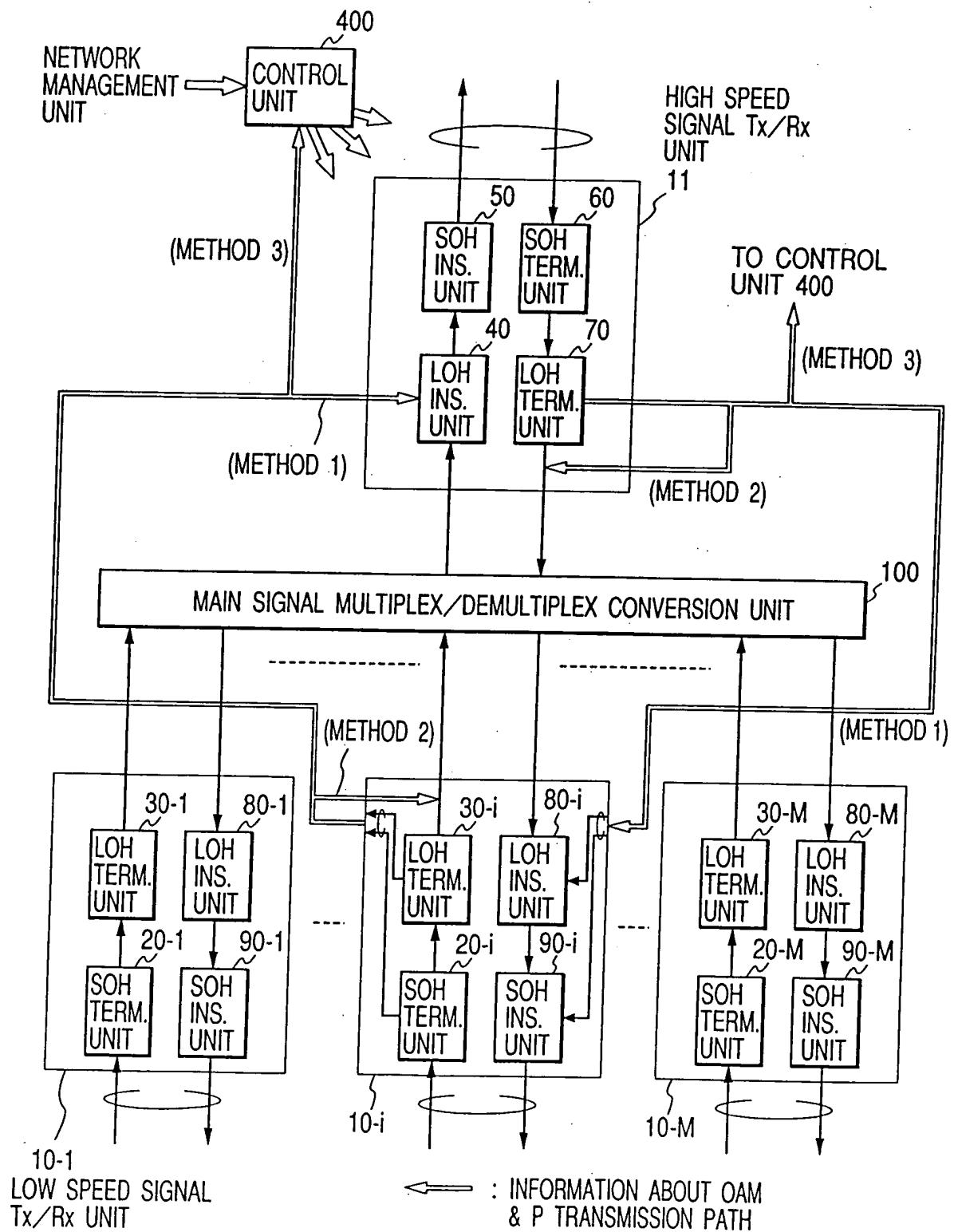


FIG. 16

